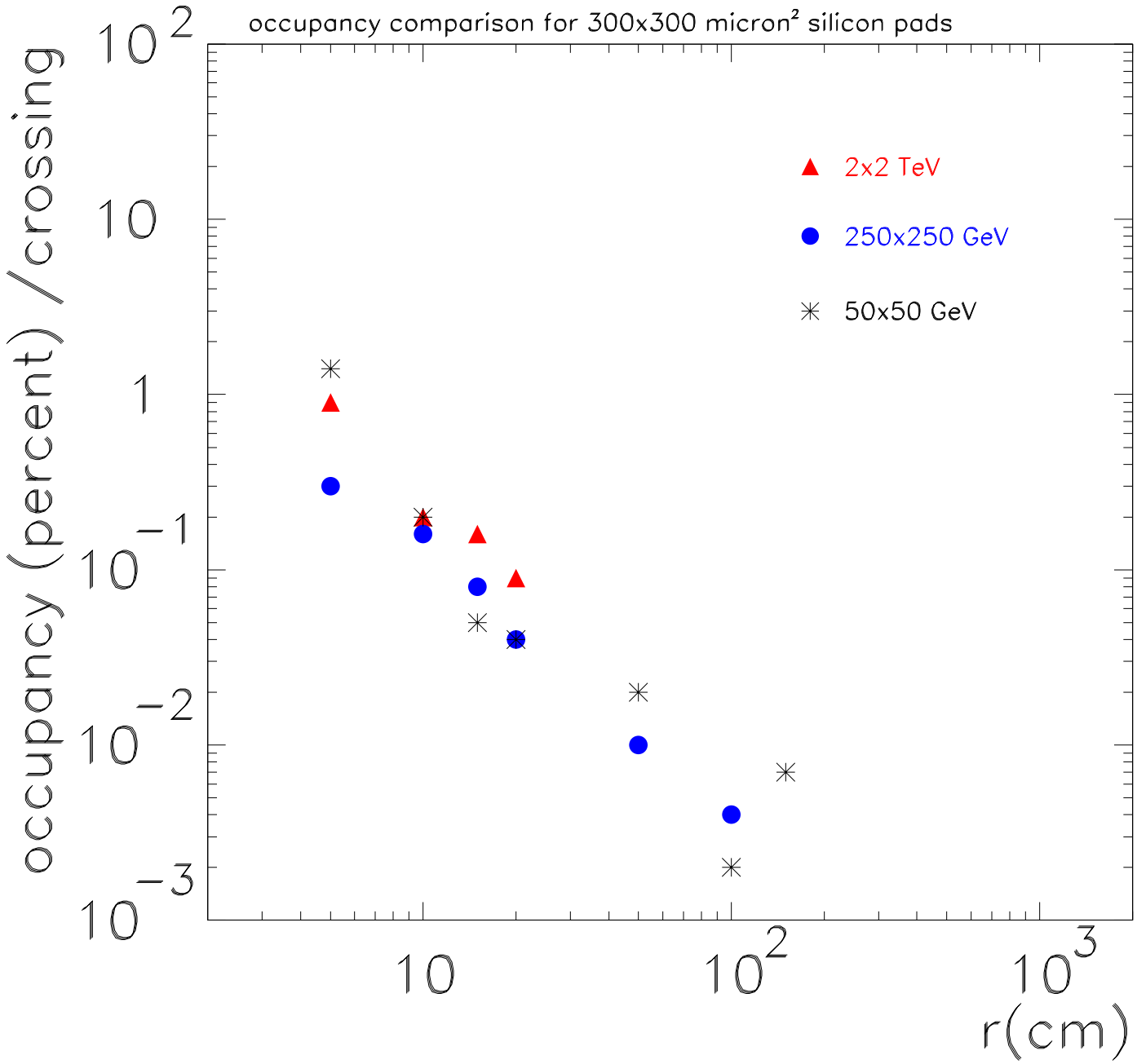
occupancy comparison for 300x300 micron² silicon pads
2x2 TeV
250x250 GeV
50x50 GeV
occupancy (percent) /crossing
r(cm)
10 2
10
1
10 −1
10 −2
10 −3
10
10 2
10 3